PATENT ABSTRACTS OF JAPAN

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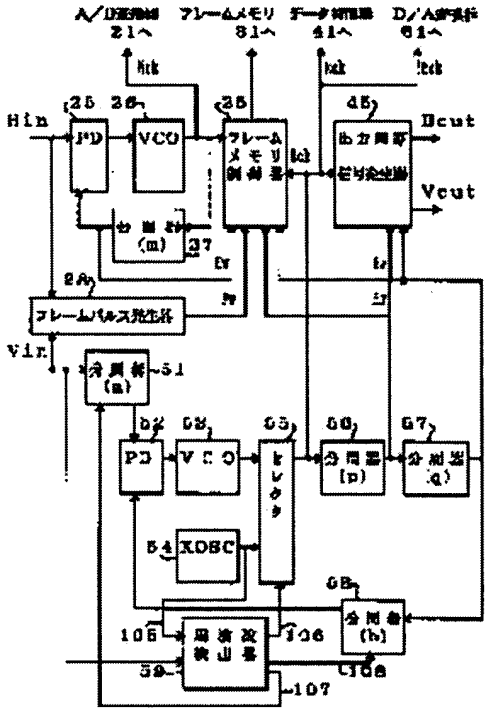
(54) VIDEO SIGNAL CONVERTER

(57)Abstract:

PURPOSE: To prevent a white/read operation from outrunning wind being outrun by setting a field or a frame frequency of a video signal of a 2nd video signal system so as to have a predetermined synchronization relation with that of a 1st video signal system.

CONSTITUTION: A write clock Wck phase-locked to a horizontal synchronizing signal Hin and having a frequency of a multiple of (m) is outputted from a VCO 26 of a PLL circuit and an input video signal is converted at an A/D converter 21 based on the clock Wck. A frame memory controller 35 controls write of a digitized input video signal to a frame memory 31 based on a clock Hw outputted from a frequency divider 27 of the PLL circuit and a frame pulse Fw outputted from a frame pulse generator 28. A write address counter of the controller 35 is reset for each frame by the pulse Fw. RGB video data written in the memory 31 are sequentially read by other TV under the control of the controller 35.



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